Completing Lab 1 of Chapter 3 should take about 30 minutes after you have completed Chapter 2 (and you have configured your lab environment and installed the tutorial's design files).

Lab 1 provides an overview of how to construct and run a testbench for a design and introduces the debugging environment for the simulator. It is useful to be aware of the concepts discussed in this lab.

Labs 2 and 3 of Ch. 3 do not work without some modification/errors. I do not recommend completing them at this stage. We shall look at arbitrary precision types in more detail in Chapter 5 of the tutorial.

Specific notes:
- For Lab 1, modify the `set_part` command in the `run_hls.tcl` script to use the **xc7z020clg484-1** device